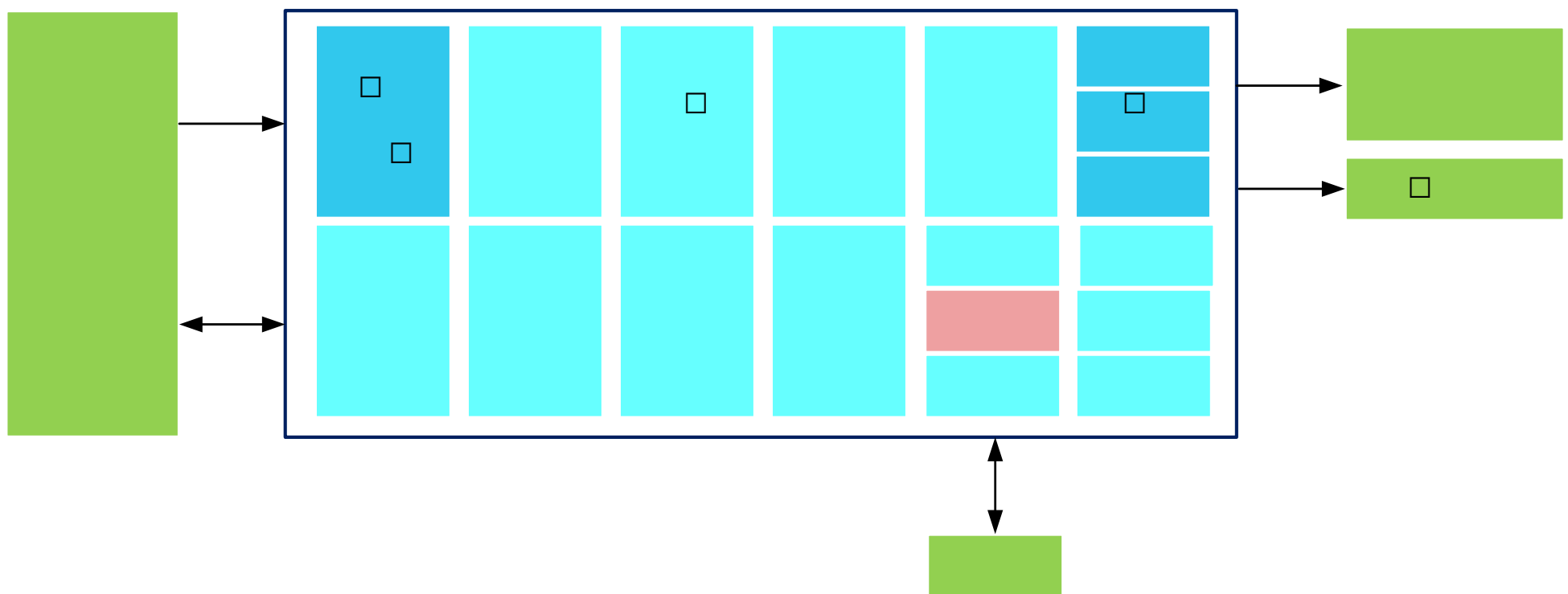


FH8323 is a 2M high dynamic range image processing chip, the maximum image processing capacity can reach 1920x1080@60fps, support 3/4-frame synthesis HDR(140dB), support analog HD and MIPI/DVP digital output interface. Suitable for in-vehicle applications such as reversing, surround view, and streaming media cameras.

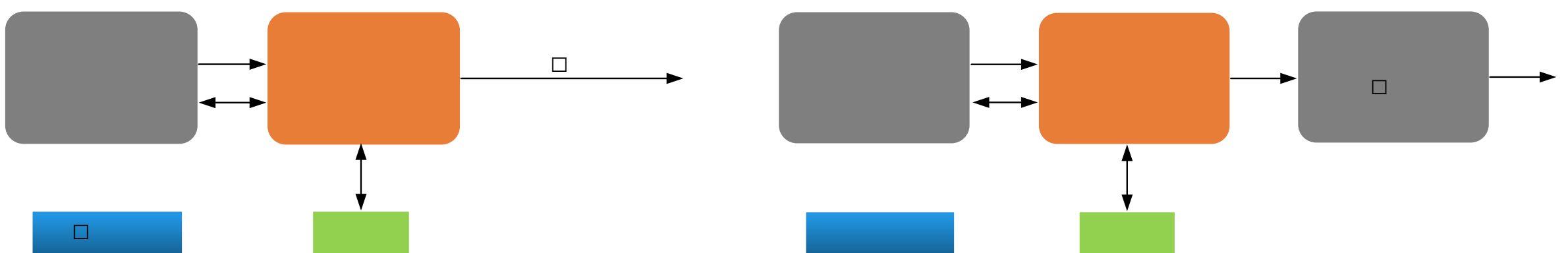
## Characteristics

Video Input	Max Resolution 1920x1080, and 120fps for 4-frame synthesis	Video Output	Support HD-Analog video output
	Support MIPI CSI-2 input		Support BT656-Like output, reach to 1080P60fps
	Support 10/12/14/16 bit raw data		Support CSI-2 MIPI output
ISP Function	Support exposure synchronization with Sensor	Security	Support OTP(One time programming)
	Support up to 4 frames wide dynamic synthesis, Support line-by-line, large and small pixels and different Conversion Gain	Peripheral	1 CH SPI
	Support lens shading compensation		1 CH I2C Master 1 CH I2C Slave
	Picture enhancement		Some GPIO
	Support high-performance CFA filter		4 CH PWM
	Support AE/AWB	2 CH UART	
	Support gamma correction	2 CH SADC	
Support GOSD, decompression and overlay for reversing line	Other	VDD_IO 3.3/1.8V, Analog 3.3V, Digital 1.1V	
		Typical Comsumption 420mW	
		QFN68 ( 7mm x 7mm)	
		-20~ 70	

## Outline



## Typical Application



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